

UTILITY SERIAL NUMBER	PATENT DATE	FILED	MSR
08/788,560	01/24/97	CLASS 257	SUBCLASS 66
FILING DATE RULE 60		GROUP ART UNIT 2811	EXAMINER NADAV

SHUNPEI YAMAZAKI, TOKYO, JAPAN; YASUHIKO TAKEMURA, KANAGAWA, JAPAN.

**\*\*CONTINUING DATA\*\*\*\*\***

VERIFIED THIS APPLN IS A DIV OF 08/213,837 03/16/94  
WHICH IS A CON OF 07/813,071 12/26/91

**\*\*FOREIGN/PCT APPLICATIONS\*\*\*\*\***

VERIFIED	JAPAN	2-418365	12/25/90
	JAPAN	2-418364	12/25/90
	JAPAN	3-019538	01/19/91
	JAPAN	3-020508	01/21/91
	JAPAN	3-050792	02/22/91

**CPA**

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**NOTE-DISCLAIMER**  
The term of this patent shall not extend beyond the expiration date of Pat. No. 5,821,563

Foreign priority claimed as U.S. 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	AS FILED	STATE OR COUNTRY	SHEETS DRWGS.	TOTAL CLAIMS	INDEP. CLAIMS	FILING FEE RECEIVED	ATTORNEYS DOCKET NO.
Verified and Acknowledged	<input type="checkbox"/> yes <input type="checkbox"/> no	JPX	25	1	1	\$1,410.00	0756-1626	

STYBEY-FRIEDMAN-LEEDOM & FERGUSON  
2010 CORPORATE RIDGE  
SUITE 600  
MCLEAN VA 22102

SEMICONDUCTOR DEVICE AND METHOD FOR FORMING THE SAME Having source/Channel or Drain/Channel Boundary Regions

U.S. DEPT. OF COMM./PAT. & TM PTO-436L (Rev.12-84)

PARTS OF APPLICATION FILED SEPARATELY		Applications Examiner	
NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim
80		80	1
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
80		25	46
Label Area		Print Fig.	14
TOM THOMAS SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800		ISSUE BATCH NUMBER	
Primary Examiner		PREPARED FOR ISSUE	
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PTO-436L  
(Rev. 8-82)

(FACE)